

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	10752	714/724,725,726,727,729,733,734,30;716/4.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:23
L2	322824	test access port or TAP	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:24
L3	2	TAP control selector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:24
L4	788	automatic tester	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:24
L5	6	TAP master controller	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:24

L6	1765334	integrated circuit or IC	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:25
L7	14	I2 and I4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:25
L8	1	I7 and I5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:25
L9	1	I6 and I8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:26
L10	3188	test controller	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:26
L11	1	I10 and I5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:27

L12	1684	(multiplex\$3 or MUX or select\$3 or switch\$3 or control\$4 or gate\$1) same (TAP or test access port near\$5 automatic tester) same (TAP master controller or TAP master or TAP controller)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:29
L13	229	I10 and I12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:30
L14	203	I13 and I6	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:30
L15	203	I14 and I2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:31
L16	133	I14 and I1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:31
L17	350	JTAG signal\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:32

L18	7	I16 and I17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:32
L19	819	detect\$3 same (condition or error or fault) same (integrated circuit or IC) same (TAP or test access port)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:33
L20	1878	(freez\$3 or stop\$4 or disabl\$3 or interrupt\$3) same (integrated circuit or IC near5 logic state) same (detected condition or error or fault or failure)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:36
L21	2	I19 and I20	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/12 14:36

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